

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

RAJESH R. SHAH, ET AL.

Application No.: 09/450,381

Filed: November 29, 1999

For: **A Cluster With Multiple Paths Between
Hosts And I/O Controllers**

Art Group: 2126

Examiner: Anya, Charles E.

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Technology Center 2100

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Commissioner for Patents
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In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

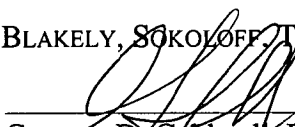
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Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

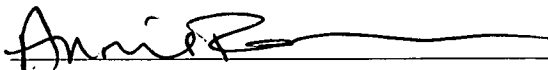
Date: September 13, 2004



Gregory D. Caldwell, Reg. No. 39,926

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (503) 439-8778

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet

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of

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Complete if Known

Application Number	09/450,381
Filing Date	November 29, 1999
First Named Inventor	Rajesh R. Shah
Art Unit	2126
Examiner Name	Anyia, Charles E.
Attorney Docket Number	42390P7719

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	5	PCI Local Bus Specification, Production Version, Revision 2.1, June 1, 1995, 1992, 1993, 1995 PCI Special Interest Group.	
	6	Virtual Interface Architecture Specification, Draft Revision 1.0, December 4, 1997, 1997 Compaq Computer Corp., Intel Corporation, Microsoft Corporation.	
	7	Next Generation I/O Link Architecture Specification: Fabric Service Interface Specification, Release 1.0.0: July 20, 1999.	
	8	Next Generation I/O Link Architecture Specification: HCA Specification, Release 1.0.0: July 20, 1999.	
	9	Next Generation I/O Link Architecture Specification: Link Specification, Release 1.0.0: July 20, 1999.	
	10	Next Generation I/O: Principles of Operation, Release 1.0.0: July 20, 1999.	
	11	Next Generation I/O Link Architecture: Switch Specification, Release 1.0.0: July 20, 1999.	
	12	Next Generation I/O Link Architecture Specification: Channel Interface Specification, Release 1.0.0: July 20, 1999.	

Examiner Signature		Date Considered	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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